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James J. Bade (Depositor's Name)  
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 October 6, 2006 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/023,123	10/30/2001	Takahiro Fujita	FUJIS 19, 099	2408

TITLE OF INVENTION: PARALLEL SIGNAL AUTOMATIC PHASE ADJUSTING CIRCUIT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$0	\$1700	12/07/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
GHEBRETINSAB, TEMESGHEN	2611	375-371000

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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Fujitsu Limited

Kawasaki, Japan

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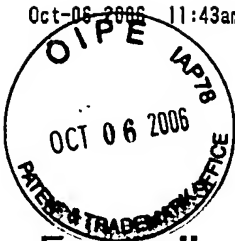
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1. Commissioner for Patents	United States Patent and Trademark Office	(571) 273-2885	

Date	Client/Matter Number
October 6, 2006	100794-00051
From	Attorney Number
James J. Dade	03290
Phone	Fax
212.940.6731	(212) 940-8986

Total number of pages, including cover letter: 3  
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**Comments:****U.S. Patent Application No.: 10/023,123****PARALLEL SIGNAL AUTOMATIC PHASE ADJUSTING CIRCUIT****Our Reference No.: FUJS 19.099 (100794-00051)****FEE(S) TRANSMITTAL****For Messenger Department Use Only**

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